



TGD N-Channel Super Trench Power MOSFET

Description

The TGDP6080AG uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

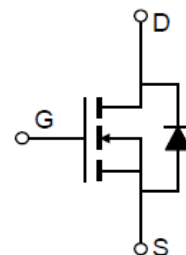
General Features

- $V_{DS} = 60V, I_D = 80A$
 $R_{DS(ON)} < 4.0m\Omega @ V_{GS}=10V$ (Typ:3.5m Ω)
 $R_{DS(ON)} < 5.0m\Omega @ V_{GS}=4.5V$ (Typ:4.0m Ω)
- Excellent gate charge x $R_{DS(on)}$ product
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

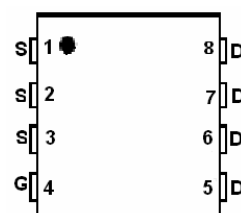
Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

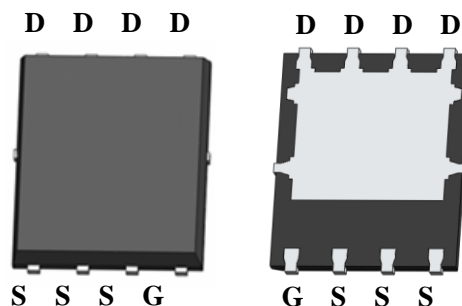
100% UIS TESTED!
100% ΔV_{ds} TESTED!



Schematic diagram



Marking and pin assignment



Top View

Bottom View

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|------------|----------------|-----------|------------|----------|
| TGDP6080AG | TGDP6080AG | DFN5X6-8L | - | - | - |

Absolute Maximum Ratings ($T_C=25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|---------------------|------------|---------------|
| Drain-Source Voltage | V_{DS} | 60 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous (Silicon Limited) | I_D | 80 | A |
| Drain Current-Continuous($T_C=100^\circ C$) | $I_D (100^\circ C)$ | 58 | A |
| Pulsed Drain Current | I_{DM} | 320 | A |
| Maximum Power Dissipation | P_D | 85 | W |
| Derating factor | | 0.68 | W/ $^\circ C$ |
| Single pulse avalanche energy ^(Note 5) | E_{AS} | 400 | mJ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | $^\circ C$ |



Thermal Characteristic

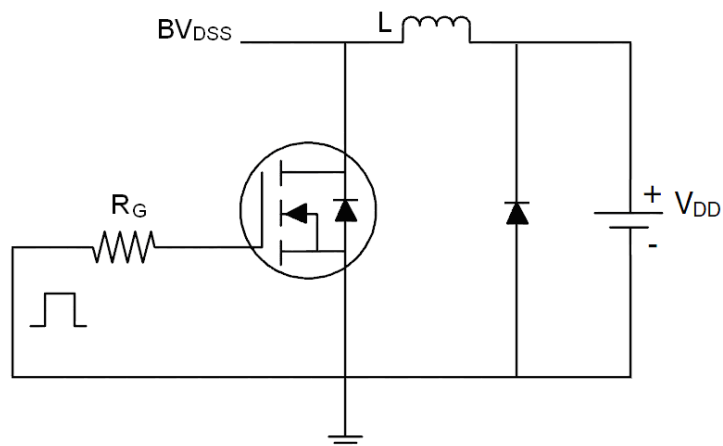
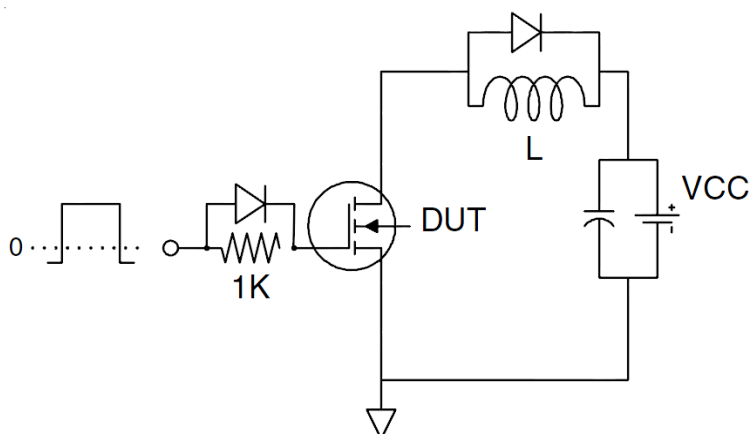
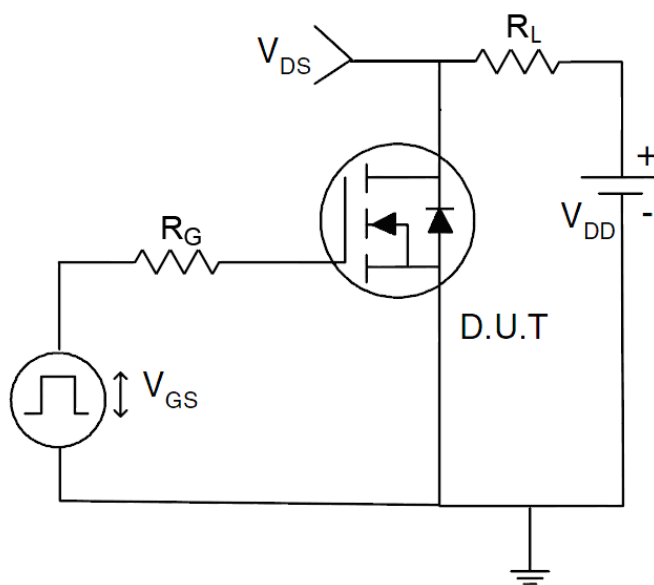
| | | | |
|--|-----------------|------|----------------------|
| Thermal Resistance, Junction-to-Case ^(Note 2) | $R_{\theta JC}$ | 1.47 | $^{\circ}\text{C/W}$ |
|--|-----------------|------|----------------------|

Electrical Characteristics ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

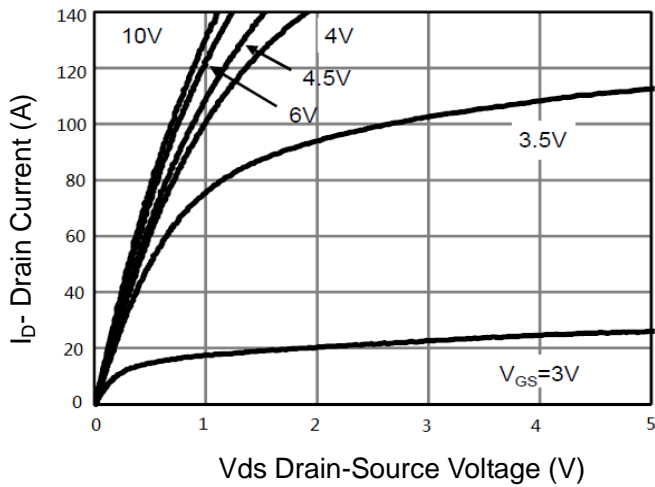
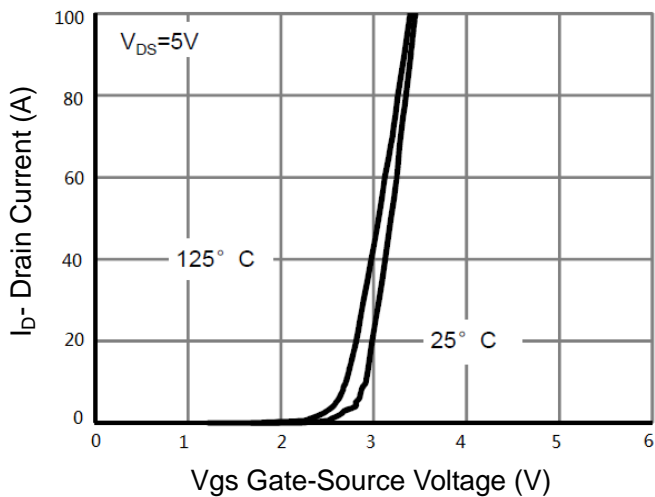
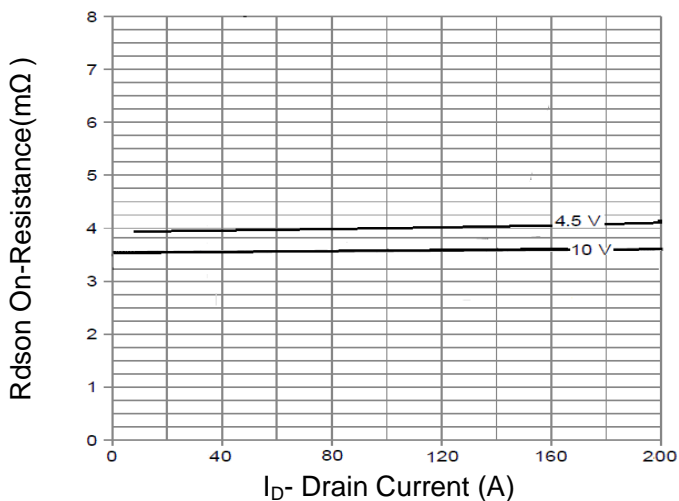
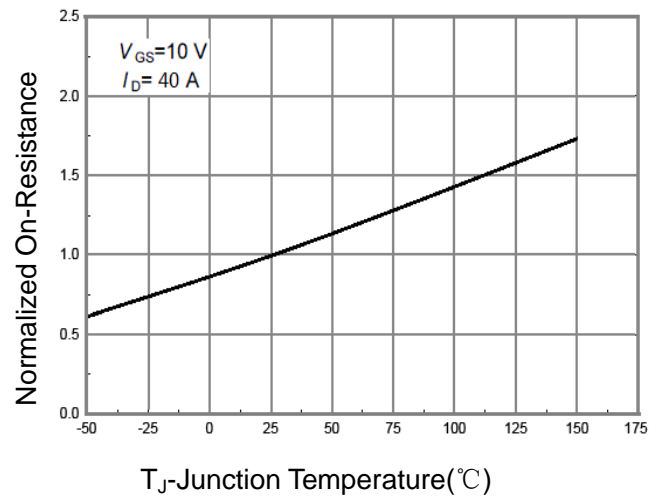
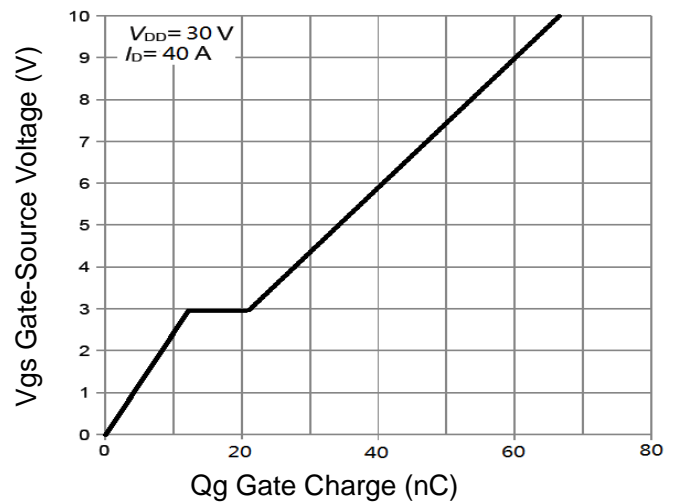
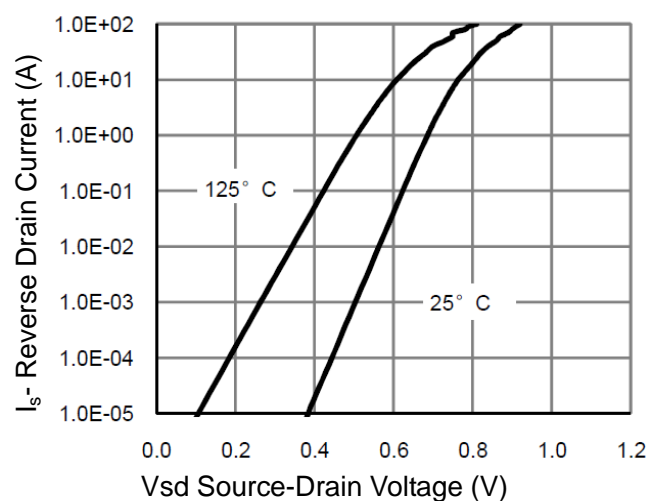
| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|---------------------|--|-----|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V I _D =250μA | 60 | | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =60V, V _{GS} =0V | - | - | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics ^(Note 3) | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 1.0 | 1.7 | 2.4 | V |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =10V, I _D =40A | - | 3.5 | 4.0 | mΩ |
| | | V _{GS} =4.5V, I _D =40A | - | 4.0 | 5.0 | mΩ |
| Forward Transconductance | g _{FS} | V _{DS} =10V, I _D =40A | 40 | - | - | S |
| Dynamic Characteristics ^(Note4) | | | | | | |
| Input Capacitance | C _{ISS} | V _{DS} =30V, V _{GS} =0V, F=1.0MHz | - | 4000 | - | PF |
| Output Capacitance | C _{OSS} | | - | 680 | - | PF |
| Reverse Transfer Capacitance | C _{RSS} | | - | 23 | - | PF |
| Switching Characteristics ^(Note 4) | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =30V, I _D =40A V _{GS} =10V, R _G =4.7Ω | - | 11 | - | nS |
| Turn-on Rise Time | t _r | | - | 5 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | - | 56 | - | nS |
| Turn-Off Fall Time | t _f | | - | 12 | - | nS |
| Total Gate Charge | Q _g | V _{DS} =30V, I _D =40A, V _{GS} =10V | - | 67 | | nC |
| Gate-Source Charge | Q _{gs} | | - | 12 | | nC |
| Gate-Drain Charge | Q _{gd} | | - | 8.5 | | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage ^(Note 3) | V _{SD} | V _{GS} =0V, I _S =80A | - | | 1.2 | V |
| Diode Forward Current ^(Note 2) | I _S | | - | - | 80 | A |
| Reverse Recovery Time | t _{rr} | T _J = 25°C, I _F = I _S di/dt = 100A/μs ^(Note3) | - | 48 | | nS |
| Reverse Recovery Charge | Q _{rr} | | - | 60 | | nC |

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^{\circ}\text{C}, V_{DD}=30V, V_G=10V, L=0.5\text{mH}, R_G=25\Omega$

Test Circuit
1) E_{AS} test Circuit

2) Gate charge test Circuit

3) Switch Time Test Circuit


Typical Electrical and Thermal Characteristics


Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 Rdson- Drain Current

Figure 4 Rdson-Junction Temperature

Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward

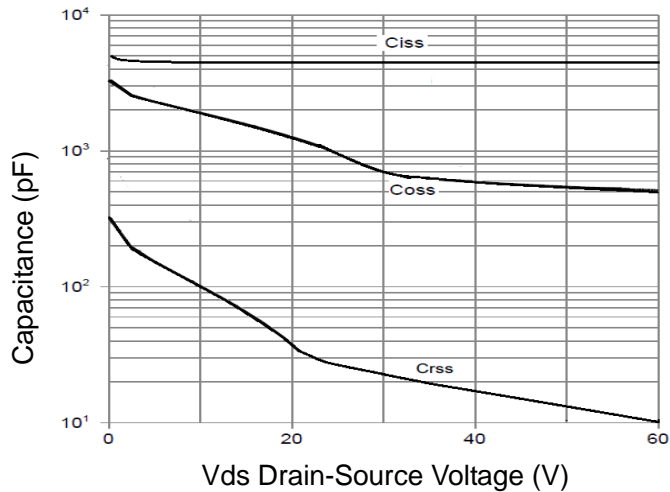


Figure 7 Capacitance vs Vds

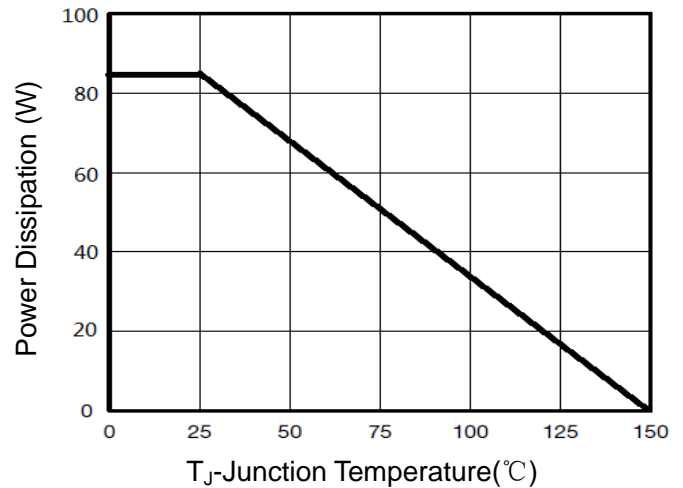


Figure 9 Power De-rating

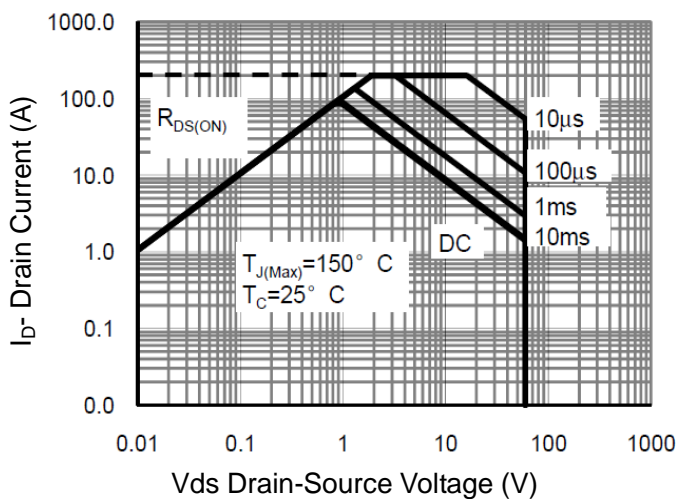


Figure 8 Safe Operation Area

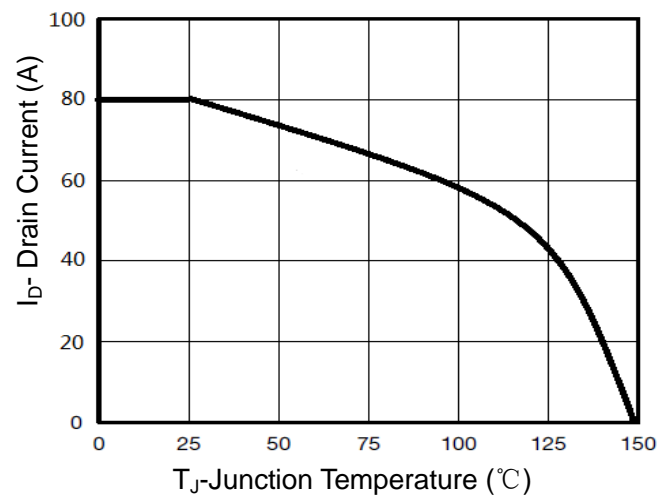


Figure 10 Current De-rating

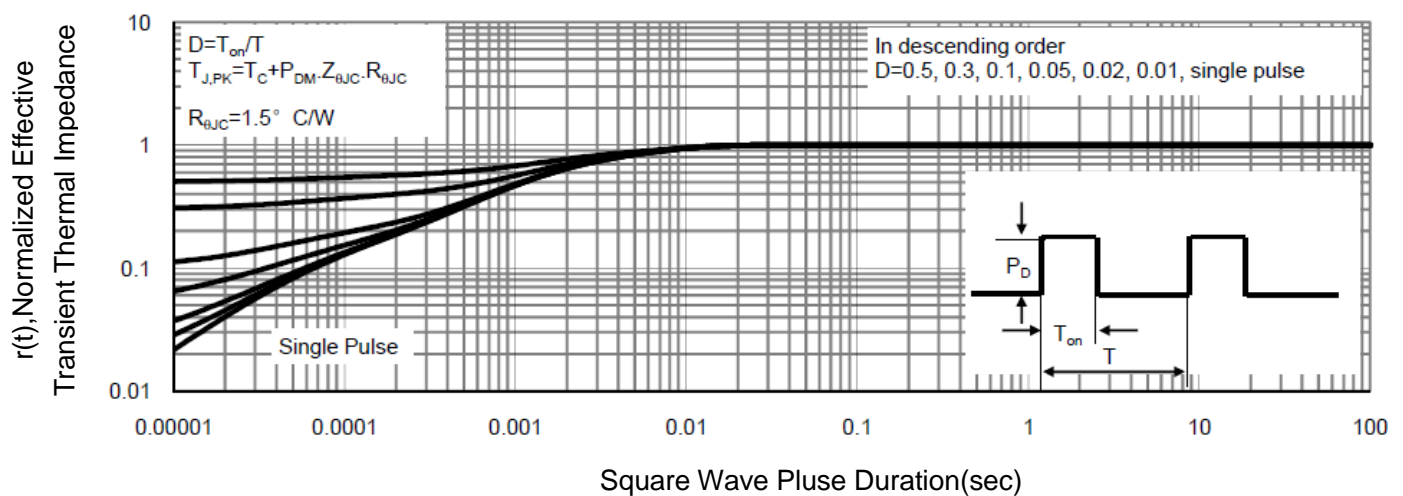
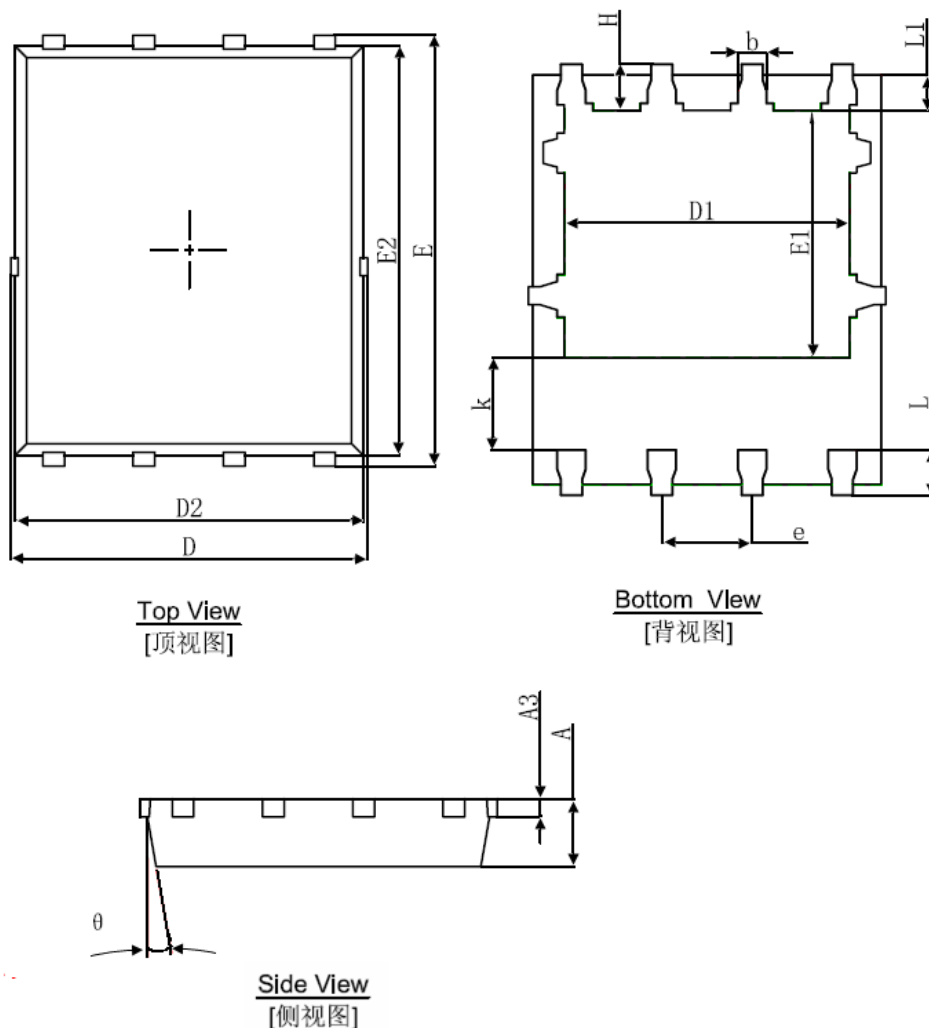


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information


| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|----------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 0.900 | 1.000 | 0.035 | 0.039 |
| A3 | 0.254REF. | | 0.010REF. | |
| D | 4.944 | 5.096 | 0.195 | 0.201 |
| E | 5.974 | 6.126 | 0.235 | 0.241 |
| D1 | 3.910 | 4.110 | 0.154 | 0.162 |
| E1 | 3.375 | 3.575 | 0.133 | 0.141 |
| D2 | 4.824 | 4.976 | 0.190 | 0.196 |
| E2 | 5.674 | 5.826 | 0.223 | 0.229 |
| k | 1.190 | 1.390 | 0.047 | 0.055 |
| b | 0.350 | 0.450 | 0.014 | 0.018 |
| e | 1.270TYP. | | 0.050TYP. | |
| L | 0.559 | 0.711 | 0.022 | 0.028 |
| L1 | 0.424 | 0.576 | 0.017 | 0.023 |
| H | 0.574 | 0.726 | 0.023 | 0.029 |
| θ | 8° | 12° | 8° | 12° |