



## FEATURES

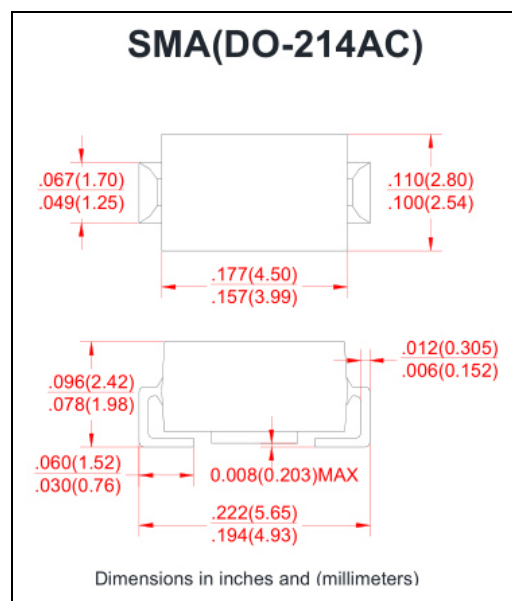
- Low profile surface mount package
- Built in strain relief
- High switching speed
- Low voltage drop, high efficiency
- For use in low voltage high frequency inverters, Free willing ,and polarity protection applications
- Guardring for over voltage protection

## MECHANICAL DATA

- Case: Transfer molded plastic
- Epoxy: UL 94V-0 rate flame retardant
- Lead :Solder plated, solderable per MIL-STD-750 method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.002 ounce, 0.064 gram

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

- Ratings at 25°C ambient temperature unless otherwise specified.
- Single phase, half wave, 60Hz, resistive or inductive load.
- For capacitive load derate current by 20%.



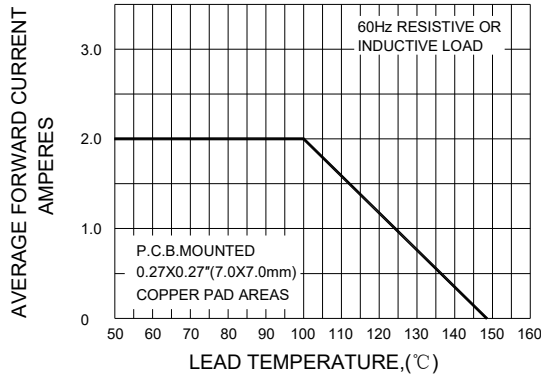
	SYMBOLS	SS215	SS220	UNIT
Maximum Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	150	200	Volts
Maximum RMS Voltage	V <sub>RMS</sub>	105	140	Volts
Maximum DC Blocking Voltage	V <sub>DC</sub>	150	200	Volts
Maximum Average Forward Rectified Current at T <sub>L</sub> see figure 1 T <sub>L</sub> =105℃	I <sub>(AV)</sub>	2		Amps
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I <sub>FSM</sub>	50		Amps
Maximum Instantaneous Forward Voltage @ 3.0A(Note1)		V <sub>F</sub>	0.85	Volts
Maximum DC Reverse Current at rated DC Blocking Voltage per element	T <sub>A</sub> = 25℃	I <sub>R</sub>	0.15	mA
	T <sub>A</sub> = 100℃		1.5	
Typical Thermal Resistance (Note 2)	R <sub>θJA</sub>	55		℃/W
	R <sub>θJL</sub>	12		
Operating Junction Temperature	T <sub>J</sub>	150		℃
Storage Temperature Range	T <sub>STG</sub>	(-55 to +150)		℃

### Notes:

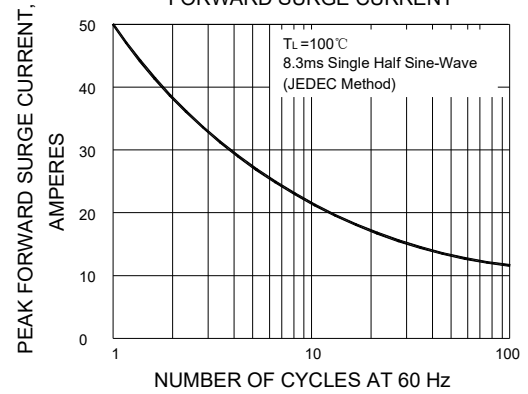
1. Pulse test: 300  $\mu\text{s}$  pulse width, 1% duty cycle
2. PCB mounted with 0.2"  $\times$  0.2" (5.0cm  $\times$  5.0cm) copper pads



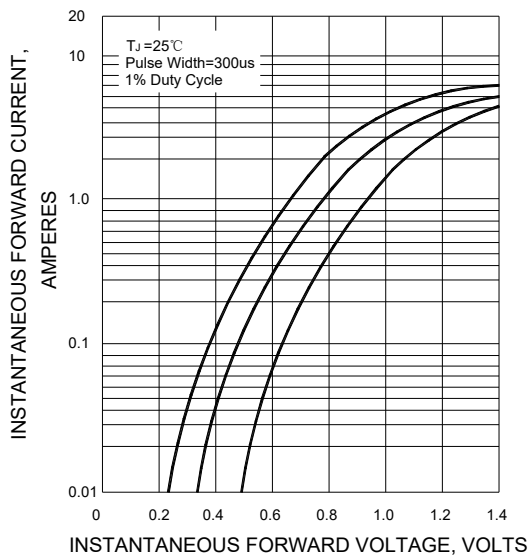
F1G.1-FORWARD CURRENT DERATING CURVE



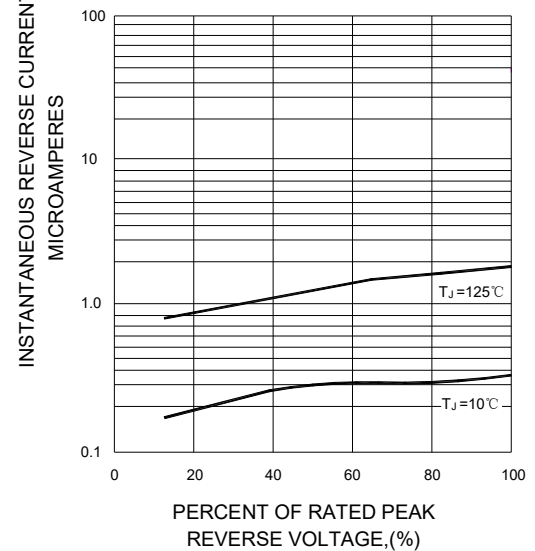
F1G.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT



F1G.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS



F1G.4-TYPICAL REVERSE CHARACTERISTICS



F1G.5-TYPICAL JUNCTION CAPACITANCE

